



B
Rfw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Applicant :	Tsiu Chan	Docket No :	97-C-108C1
Serial No :	09/160,824	Group No :	2826
Filed :	September 25, 1998	Examiner :	Thomas L. Dickey
For :	STACKED MULTI-COMPONENT INTEGRATED CIRCUIT MICROPROCESSOR	Confirmation No :	6763

TRANSMITTAL LETTER

Mail Stop Issue Fee
Attention: Official Draftsman
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability (PTO-37) mailed June 29, 2004, for the above-identified patent application, please find enclosed for filing one (1) sheets of formal drawings.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted

Mario J. Donato, Jr.
Reg. No. 37,816
Attorney for Applicant

STMicroelectronics, Inc.
1310 Electronics Drive/MS 2346
Carrollton, TX 75006
972-466-7503

CERTIFICATE OF MAILING 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage, in an enveloped addressed to: Mail Stop Issue Fee Attention: Official Draftsman Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-14501 on the date below:

September 29, 2004
Date

Angie Rodriguez
Signature